



2125

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
ELFIDO COSS, JR. ET AL.

Serial No.: 09/479,852

Filed: 1/7/2000

For: A METHOD FOR REQUESTING TRACE
DATA REPORTS FROM FDC
SEMICONDUCTOR FABRICATION
PROCESSES

Group Art Unit: 2125

Examiner: PAUL L. RODRIGUEZ

Atty. Dkt. No.: 2000.021100/TT3263/JAP

10/B
P.P.
2/12/04

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Technology Center 2100

RESPONSE TO OFFICE ACTION DATED OCTOBER 29, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 29, 2004.

Yolanda Murillo
Yolanda Murillo

This paper is submitted in response to the Office Action dated October 29, 2003 for which the three-month date for response is January 29, 2004.

It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason, the Director is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT3263.

Reconsideration of the application is respectfully requested.

AMENDMENTS TO SPECIFICATION

Please replace the paragraph beginning at p. 10, line 27 – p. 11, line 2 with the following amended paragraph:

Referring now to both Figures 4 and 5, the APC System comprises, more particularly, a network of processing modules. These processing modules are sometimes referred to as

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